

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the present Application:

Listing of Claims

- 1 1. (Currently Amended) A buffer circuit that outputs a bias signal that varies
2 linearly with an input signal for biasing a cascode LNA, the buffer circuit comprising:
3 an input emitter-follower stage that receives an the input signal and produces the bias
4 signal at an output terminal;
5 a gain stage coupled to the emitter-follower stage;
6 a load coupled to the emitter-follower stage; and
7 a feedback circuit coupled to the load and the gain stage.

- 1 2. (Currently Amended) A bias circuit that outputs a bias signal that varies
2 linearly with an input signal for biasing an amplifier, the bias circuit comprising:
3 an input stage that receives an the input signal and produces the bias signal at an
4 output terminal that is coupled to a gain stage;
5 a load coupled to the input stage at a first terminal; and
6 a feedback circuit coupled between the first terminal and the gain stage.

- 1 3. (Original) The bias circuit of claim 2, wherein the input stage comprises an
2 emitter-follower input stage.

- 1 4. (Original) The bias circuit of claim 2, wherein the gain stage comprises a
2 common-emitter gain stage that has a base terminal.

- 1 5. (Original) The bias circuit of claim 4, wherein the feedback circuit is coupled
2 between the first terminal and the base terminal.

- 1 6. (Original) The bias circuit of claim 5, wherein the feedback circuit comprises:

2 first and second diodes; and

3 a capacitor coupled to the first and second diodes.

1 7. (Original) The bias circuit of claim 2, where in the feedback circuit comprises:
2 first and second diodes; and
3 a capacitor coupled to the first and second diodes.

1 8. (Original) The bias circuit of claim 2, wherein the feedback circuit comprises
2 a current source coupled to the gain stage.